

Date: Oct 07th, 2019

Changes:

- CP-J – V2.41, Make detection and connectivity more robust. Display correct number of mixer matrix columns.
- Model E-3A – V1.14, updated DSP code for a bug related to mixer matrix input maps. Prevent one output being set to multiple mixer matrix outputs.
- Model E-2A – V1.04, updated DSP code for a bug related to mixer matrix input maps. Prevent one output being set to multiple mixer matrix outputs.
- Model M – V1.16, updated DSP code for a bug related to mixer matrix input maps. Prevent one output being set to multiple mixer matrix outputs. DSP revised to deal with 96kHz output problems.
- Model N – V1.18, updated DSP code for a bug related to mixer matrix input maps. Prevent one output being set to multiple mixer matrix outputs. DSP revised to deal with 96kHz output problems.

Firmware Versions:

Model E

Microcontroller : V2.3
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.41 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.41 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.22
FPGA : V3.3
CP-J : V2.41 Standalone and browser application

Model E-2

Microcontroller : V1.09
FPGA : V0.1
CP-J : V2.41 Standalone and browser application

Model E-2A

Microcontroller : V1.04
FPGA : V0.40
CP-J : V2.41 Standalone and browser application

Model E-3

Microcontroller : V1.17
FPGA : V0.6
CP-J : V2.41 Standalone and browser application

Model E-3A

Microcontroller : V1.14
FPGA : V0.8
CP-J : V2.41 Standalone and browser application

Dugan-MY16

Microcontroller : V1.14
FPGA : V1.5
CP-J : V2.41 Standalone and browser application

Dugan-VN16

Microcontroller : V1.03
FPGA : V1.4
CP-J : V2.41 Standalone and browser application

Model M

Microcontroller : V1.16
FPGA : V0.8
CP-J : V2.41 Standalone and browser application

Model N

Microcontroller : V1.18
FPGA : V0.7
CP-J : V2.41 Standalone and browser application

Model K

Microcontroller : V0.6
CP-J : V2.41

CP-i : V2.1

Date: Jan 26th, 2018

Changes:

- CP-J – V2.38, Model M and N can change number of automix channels.
- Model E-3A – V1.12, updated DSP code for a minor bug related to ADAT mode.
- Model M – V1.12, fixed scenes overlapped in memory problem.
- Model N – V1.13, fixed scenes overlapped in memory problem.
- Model K – V0.6, fixed problem talking to Model E-2A.

Firmware Versions:

Model E

Microcontroller : V2.3
DSP : V1.7
WebUI : V1.5 Browser application

CP-J : V2.38 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.38 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.22
FPGA : V3.3
CP-J : V2.38 Standalone and browser application

Model E-2

Microcontroller : V1.09
FPGA : V0.1
CP-J : V2.38 Standalone and browser application

Model E-3

Microcontroller : V1.17
FPGA : V0.6
CP-J : V2.38 Standalone and browser application

Model E-3A

Microcontroller : V1.12
FPGA : V0.8
CP-J : V2.38 Standalone and browser application

Dugan-MY16

Microcontroller : V1.14
FPGA : V1.5
CP-J : V2.38 Standalone and browser application

Dugan-VN16

Microcontroller : V1.03
FPGA : V1.4
CP-J : V2.38 Standalone and browser application

Model M

Microcontroller : V1.12
FPGA : V0.6
CP-J : V2.38 Standalone and browser application

Model N

Microcontroller : V1.13
FPGA : V0.6
CP-J : V2.38 Standalone and browser application

Model K

Microcontroller : V0.6
CP-J : V2.38

CP-i : V2.1

Date: Nov 1st, 2017

Changes:

- Model E-1A won't process master action from other units unless units are linked.
- Model E3A – Fixed mixer matrix labels. Mixer crosspoints upper bounded to 0dB.
Fixed front panel character spacing. Master actions are now mutually exclusive.
- Model M and Model N – Added mixer matrix support. Fixed front panel character spacing.
Master actions are now mutually exclusive.
- CP-J –Numerous bug fixes and add support for mixer matrix for Model-N and Model-M.

Firmware Versions:

Model E

Microcontroller : V2.3
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.37 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.37 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.22
FPGA : V3.3
CP-J : V2.37 Standalone and browser application

Model E-2

Microcontroller : V1.09
FPGA : V0.1
CP-J : V2.37 Standalone and browser application

Model E-3

Microcontroller : V1.17
FPGA : V0.6
CP-J : V2.37 Standalone and browser application

Model E-3A

Microcontroller : V1.11
FPGA : V0.7

CP-J : V2.37 Standalone and browser application

Dugan-MY16

Microcontroller : V1.14

FPGA : V1.5

CP-J : V2.37 Standalone and browser application

Dugan-VN16

Microcontroller : V1.03

FPGA : V1.4

CP-J : V2.37 Standalone and browser application

Model M

Microcontroller : V1.11

FPGA : V0.5

CP-J : V2.37 Standalone and browser application

Model N

Microcontroller : V1.12

FPGA : V0.5

CP-J : V2.37 Standalone and browser application

Model K

Microcontroller : V0.5

CP-J : V2.37

CP-i : V2.1

Date: July 13th, 2017

Changes:

- All updated units won't process master action from other units unless units are linked.
- Model E2 – Fixed music reference bug when in 12-channel mode. Wait one second after power up before boot.
- Model M – Fixed minor front panel bug.
- Model N – Refreshed all Dante RX channel name at boot.
- Model E-3A – New product released.
- CP-J – Numerous bug fixes and add support for Model E-3A.

Firmware Versions:

Model E

Microcontroller : V2.3

DSP : V1.7

WebUI : V1.5 Browser application

CP-J : V2.35 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.35 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.21
FPGA : V3.3
CP-J : V2.35 Standalone and browser application

Model E-2

Microcontroller : V1.09
FPGA : V0.1
CP-J : V2.35 Standalone and browser application

Model E-3

Microcontroller : V1.17
FPGA : V0.6
CP-J : V2.35 Standalone and browser application

Model E-3A

Microcontroller : V1.00
FPGA : V0.6
CP-J : V2.35 Standalone and browser application

Dugan-MY16

Microcontroller : V1.14
FPGA : V1.5
CP-J : V2.35 Standalone and browser application

Dugan-VN16

Microcontroller : V1.03
FPGA : V1.4
CP-J : V2.35 Standalone and browser application

Model M

Microcontroller : V1.08
FPGA : V0.5
CP-J : V2.35 Standalone and browser application

Model N

Microcontroller : V1.09
FPGA : V0.4
CP-J : V2.35 Standalone and browser application

Model K

Microcontroller : V0.44

CP-J : V2.35

CP-i : V2.1

Date: July 12th, 2016

Changes:

- Model-M – FPGA updated to fix channel parity in MADI packets.
Update front panel after master preset changes.
- Model-N – Update front panel after master preset changes.
- Model-K – Allow AutoIP when DHCP is enabled.
- CP-J – Fixed unit name issue for Model E and E1.
Unit name and channel names are not restricted to 7-bit ASCII.

Firmware Versions:

Model E

Microcontroller : V2.3
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.29 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.29 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.21
FPGA : V3.3
CP-J : V2.29 Standalone and browser application

Model E-2

Microcontroller : V1.07
FPGA : V0.1
CP-J : V2.29 Standalone and browser application

Model E-3

Microcontroller : V1.16
FPGA : V0.5
CP-J : V2.29 Standalone and browser application

Dugan-MY16

Microcontroller : V1.13
FPGA : V1.5

CP-J : V2.29 Standalone and browser application

Dugan-VN16

Microcontroller : V1.03

FPGA : V1.4

CP-J : V2.29 Standalone and browser application

Model M

Microcontroller : V1.07

FPGA : V0.5

CP-J : V2.29 Standalone and browser application

Model N

Microcontroller : V1.07

FPGA : V0.4

CP-J : V2.29 Standalone and browser application

Model K

Microcontroller : V0.44

CP-J : V2.29

CP-i : V2.1

Date: January 28th, 2016

Changes:

- Models E1A, E2, E3, M and N Fixed a minor bug related to Mute LED blinking when paired with Model-K.
- Model-E2 Fixed a bug related to music reference when in 14-channel mode.
- Model-E3 Fixed a bug related to music reference when in 12-channel mode.
- Model-M Major scene related changes. Fixed MADI channel mapping bug.
- Model-N Major scene related changes. Fixed MADI channel mapping bug.
- Model-K Lots of scene related changes. Better LED current/color calibrations.

Firmware Versions:

Model E

Microcontroller : V2.3

DSP : V1.7

WebUI : V1.5 Browser application

CP-J : V2.27 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6

DSP : V1.7

WebUI : V1.5 Browser application

CP-J : V2.27 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.21
FPGA : V3.3
CP-J : V2.27 Standalone and browser application

Model E-2

Microcontroller : V1.07
FPGA : V0.1
CP-J : V2.27 Standalone and browser application

Model E-3

Microcontroller : V1.16
FPGA : V0.5
CP-J : V2.27 Standalone and browser application

Dugan-MY16

Microcontroller : V1.13
FPGA : V1.5
CP-J : V2.27 Standalone and browser application

Dugan-VN16

Microcontroller : V1.03
FPGA : V1.4
CP-J : V2.27 Standalone and browser application

Model M

Microcontroller : V1.06
FPGA : V0.4
CP-J : V2.27 Standalone and browser application

Model N

Microcontroller : V1.05
FPGA : V0.4
CP-J : V2.27 Standalone and browser application

Model K

Microcontroller : V0.43
CP-J : V2.27

CP-i : V1.0

Date: December 1st, 2015

Changes:

- Dugan-MY16 no longer set gain to -60dB when channel is muted. Fixed slave routing issue.

- Model-M fixed clock frequency display on front panel, scene related issues.
- Model-N support added.
- Model-K support added.

Firmware Versions:

Model E

Microcontroller : V2.3
 DSP : V1.7
 WebUI : V1.5 Browser application
 CP-J : V2.23 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6
 DSP : V1.7
 WebUI : V1.5 Browser application
 CP-J : V2.25 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.19
 FPGA : V3.3
 CP-J : V2.25 Standalone and browser application

Model E-2

Microcontroller : V1.03
 FPGA : V0.1
 CP-J : V2.25 Standalone and browser application

Model E-3

Microcontroller : V1.10
 FPGA : V0.5
 CP-J : V2.25 Standalone and browser application

Dugan-MY16

Microcontroller : V1.13
 FPGA : V1.5
 CP-J : V2.25 Standalone and browser application

Dugan-VN16

Microcontroller : V1.03
 FPGA : V1.4
 CP-J : V2.25 Standalone and browser application

Model M

Microcontroller : V1.02
 FPGA : V0.4
 CP-J : V2.25 Standalone and browser application

Model N

Microcontroller : V1.01
FPGA : V0.4
CP-J : V2.25 Standalone and browser application

Model K

Microcontroller : V0.37
CP-J : V2.25

CP-i : V1.0

Date: August 3rd, 2015

Changes:

- Added Model M support.
- Dugan Control Panel updated to fix a bunch of minor problem, appearance changes and support for Model M.

Firmware Versions:

Model E

Microcontroller : V2.3
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.23 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.23 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.19
FPGA : V3.3
CP-J : V2.23 Standalone and browser application

Model E-2

Microcontroller : V1.03
FPGA : V0.1
CP-J : V2.23 Standalone and browser application

Model E-3

Microcontroller : V1.10
FPGA : V0.5
CP-J : V2.21 Standalone and browser application

Dugan-MY16

Microcontroller : V1.10
FPGA : V1.4
CP-J : V2.23 Standalone and browser application

Dugan-VN16

Microcontroller : V1.01
FPGA : V1.4
CP-J : V2.23 Standalone and browser application

Model M

Microcontroller : V1.00
FPGA : V0.3
CP-J : V2.23 Standalone and browser application

CP-i : V1.0

Date: January 22nd, 2015

Changes:

- Model-E1A, E2 and E3 all have their firmware updated to fix a linking problem when Group C is involved.
- Dugan Control Panel updated to fix the Tab order. Channel weight changed correctly when up/down arrows are used.

Firmware Versions:

Model E

Microcontroller : V2.3
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.21 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.21 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.19
FPGA : V3.3
CP-J : V2.21 Standalone and browser application

Model E-2

Microcontroller : V1.03
FPGA : V0.1

CP-J : V2.21 Standalone and browser application

Model E-3

Microcontroller : V1.10

FPGA : V0.5

CP-J : V2.21 Standalone and browser application

Dugan-MY16

Microcontroller : V1.10

FPGA : V1.4

CP-J : V2.21 Standalone and browser application

Dugan-VN16

Microcontroller : V1.01

FPGA : V1.4

CP-J : V2.21 Standalone and browser application

CP-i : V1.0

Date: January 27th, 2014

Changes:

- Model-E3 updated to Version 1.08 to fix clock selection bug: clock selection switched back to AUTO upon reboot.

Firmware Versions:

Model E

Microcontroller : V2.3

DSP : V1.7

WebUI : V1.5 Browser application

CP-J : V2.19 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6

DSP : V1.7

WebUI : V1.5 Browser application

CP-J : V2.19 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.16

FPGA : V3.3

CP-J : V2.19 Standalone and browser application

Model E-2

Microcontroller : V1.01

FPGA : V0.1

CP-J : V2.19 Standalone and browser application

Model E-3

Microcontroller : V1.08
FPGA : V0.5
CP-J : V2.19 Standalone and browser application

Dugan-MY16

Microcontroller : V1.10
FPGA : V1.4
CP-J : V2.19 Standalone and browser application

Dugan-VN16

Microcontroller : V1.01
FPGA : V1.4
CP-J : V2.19 Standalone and browser application

CP-i : V1.0

Date: December 9th, 2013

Changes:

Model-VN16 released.

Firmware Versions:

Model E

Microcontroller : V2.3
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.19 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.19 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.16
FPGA : V3.3
CP-J : V2.19 Standalone and browser application

Model E-2

Microcontroller : V1.01
FPGA : V0.1
CP-J : V2.19 Standalone and browser application

Model E-3

Microcontroller : V1.07
FPGA : V0.5
CP-J : V2.19 Standalone and browser application

Dugan-MY16

Microcontroller : V1.10
FPGA : V1.4
CP-J : V2.19 Standalone and browser application

Dugan-VN16

Microcontroller : V1.01
FPGA : V1.4
CP-J : V2.19 Standalone and browser application

CP-i : V1.0

Date: August 29th, 2013

Changes:

Dugan Utility updated to cope with devices in DHCP mode.

Firmware Versions:

Model E

Microcontroller : V2.3
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.18 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.18 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.16
FPGA : V3.3
CP-J : V2.18 Standalone and browser application

Model E-2

Microcontroller : V1.01
FPGA : V0.1
CP-J : V2.18 Standalone and browser application

Model E-3

Microcontroller : V1.07
FPGA : V0.5
CP-J : V2.18 Standalone and browser application

Dugan-MY16

Microcontroller : V1.10
FPGA : V1.4
CP-J : V2.18 Standalone and browser application

CP-i : V1.0

Date: August 7th, 2013

Changes:

All current models firmware updated to improve internal flash drive handling and firmware updates.

CP-J: fixed Model E-2 mixer matrix column display.

Firmware Versions:

Model E

Microcontroller : V2.3
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.18 Standalone application

Model E-1 Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.18 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.16
FPGA : V3.3
CP-J : V2.18 Standalone and browser application

Model E-2

Microcontroller : V1.01
FPGA : V0.1
CP-J : V2.18 Standalone and browser application

Model E-3

Microcontroller : V1.07
FPGA : V0.5
CP-J : V2.17 Standalone and browser application

Dugan-MY16

Microcontroller : V1.10
FPGA : V1.4
CP-J : V2.18 Standalone and browser application

CP-i : V1.0

Date: May 27th, 2013

Changes:

Model E-3

1.Fixed network broadcast replies so that it won't show up late in Dugan Utility. Updated network command protocol.

Model E-2

1.Fixed network broadcast replies so that it won't show up late in Dugan Utility. Updated network command protocol.

Model E-1A

1.Updated network command protocol.

Dugan-MY16

1.Updated network command protocol.

Firmware Versions:

Model E

Microcontroller : V2.3
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.17 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.17 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.15
FPGA : V3.3
CP-J : V2.17 Standalone and browser application

Model E-2

Microcontroller : V1.00
FPGA : V0.1

CP-J : V2.17 Standalone and browser application

Model E-3

Microcontroller : V1.06

FPGA : V0.5

CP-J : V2.17 Standalone and browser application

Dugan-MY16

Microcontroller : V1.10

FPGA : V1.4

CP-J : V2.17 Standalone and browser application

CP-i : V1.0

Date: May 7th, 2013

Changes:

Dugan Control Panel

1.Fixed mixer matrix table for Model E-2 and E-3. Fixed matrix row header labels.

Model E-3

1.Send out correct AES channel status information.

Model E-2

1.Added support for this new product.

Firmware Versions:

Model E

Microcontroller : V2.3

DSP : V1.7

WebUI : V1.5 Browser application

CP-J : V2.17 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.6

DSP : V1.7

WebUI : V1.5 Browser application

CP-J : V2.17 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.14

FPGA : V3.3

CP-J : V2.16 Standalone and browser application

Model E-2

Microcontroller : V1.00
FPGA : V0.1
CP-J : V2.17 Standalone and browser application

Model E-3

Microcontroller : V1.04
FPGA : V0.5
CP-J : V2.17 Standalone and browser application

Dugan-MY16

Microcontroller : V1.09
FPGA : V1.4
CP-J : V2.17 Standalone and browser application

CP-i : V1.0

Date: March 11th, 2013

Changes:

Dugan Utility

- 1.Fixed bug that caused “Unit failed to reboot!” incorrectly.

Dugan Control Panel

- 1.Variou minor labeling changes.
- 2.Fixed Channel Mode display problem for Model E and Model E-1A.

Model E-1A, Model E-3, Model Dugan-MY16

- 1.Improved ADAT reception.
- 2.Improved firmware update performance.

Firmware Versions:

Model E

Microcontroller : V2.3
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.16 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.16 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.14
FPGA : V3.3

CP-J : V2.16 Standalone and browser application

Model E-3

Microcontroller : V1.04

FPGA : V0.5

CP-J : V2.16 Standalone and browser application

Dugan-MY16

Microcontroller : V1.09

FPGA : V1.4

CP-J : V2.16 Standalone and browser application

CP-i : V1.0

Date: November 2nd, 2012

Changes:

Improvement in Dugan-Utility itself. Progress update message added to the bottom of the panel.

Model E

1. When Digital Audio mode is set for the ADAT interface, the unit boots up in Link mode for a short period before switching to Digital Audio mode. During this short period, non-audio data being sent out the ADAT cable. This new firmware fixed this issue.

Dugan Control Panel

1. Minor labeling changes.

Firmware Versions:

Model E

Microcontroller : V2.3

DSP : V1.7

WebUI : V1.5 Browser application

CP-J : V2.15 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.6

DSP : V1.7

WebUI : V1.5 Browser application

CP-J : V2.15 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.13

FPGA : V3.2

CP-J : V2.15 Standalone and browser application

Model E-3

Microcontroller : V1.03
FPGA : V0.4
CP-J : V2.15 Standalone and browser application

Dugan-MY16

Microcontroller : V1.08
FPGA : V1.3
CP-J : V2.15 Standalone and browser application

CP-i : V1.0

Date: October 8th, 2012

Changes:

DuganCP-J

1. Model E-3 clock source list changed to show descending priority when “Automatic” is selected.
2. 8/14 or 8/16 channel selection changed from a slider switch to a pushbutton.
3. “set to defaults” button added to the mix bus panel.
4. Fixed problem for countries that use ',' as decimal separator.
5. Text box values now can be changed by dragging the mouse pointer up/down or left/right.

Model E-1A

1. Signal present LED behavior slightly modified to match the user manual.
2. When in analog audio mode, audio ADAT outputs set to analog audio inputs.
3. When in ADAT audio mode, analog audio outputs set to ADAT audio inputs.
4. Default safety threshold changed to -90dBu.

Model E-3

1. Signal present LED behavior slightly modified to match the user manual.
2. Allow mix bus parameters to be reset from CP-J and CP-i.
3. When in AES audio mode, audio ADAT outputs set to AES audio inputs.
4. When in ADAT audio mode, AES audio outputs 1 to 8 set to ADAT audio inputs.
5. Default safety threshold changed to -90dBu.

Model Dugan-MY16

1. Signal present LED behavior slightly modified to match the user manual.
2. Default safety threshold changed to -90dBu.

Firmware Versions:

Model E

Microcontroller : V2.2
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.14 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.14 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.13
FPGA : V3.2
CP-J : V2.14 Standalone and browser application

Model E-3

Microcontroller : V1.03
FPGA : V0.4
CP-J : V2.13 Standalone and browser application

Dugan-MY16

Microcontroller : V1.08
FPGA : V1.3
CP-J : V2.14 Standalone and browser application

CP-i : V1.0

Date: August 16th, 2012

Bug Fixes:

DuganCP-J

1. Manually added device list behavior changed. Tooltips of the “+” button will now show the manually added devices. Shift-Click the “+” button will remove the devices from the list and also from persistent storage.

Dugan-Utility itself

1. “Store CP-J to Unit” button works correctly now.

Firmware Versions:

Model E

Microcontroller : V2.2
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.13 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.13 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.12
FPGA : V3.1
CP-J : V2.13 Standalone and browser application

Model E-3

Microcontroller : V1.01
FPGA : V0.3
CP-J : V2.13 Standalone and browser application

Dugan-MY16

Microcontroller : V1.07
FPGA : V1.2
CP-J : V2.12 Standalone and browser application

CP-i : V1.0

Date: August 9th, 2012

Bug Fixes:

Model E-1A, Model E-3 and Dugan-MY16

1.FPGA updated so that ADAT user bits are all set to 0 so that ADAT bit streams would be interpreted correctly as 48kHz by downstream devices.

Model E-1A

1.Fixed audio anomaly when in 16-channel ADAT audio mode.
2.When in Analog audio mode, analog audio inputs are replicated at the audio ADAT outputs.
3.When in ADAT audio mode, the audio inputs are replicated at the analog outputs.

Model E-3

1.Fixed NOM gain limit display problem.

Dugan CP-J

1.Added CTRL-Click of the “+” button to enable/disable “Auto Search Mode”.
2.Added SHIFT-Click of the “+” button to forget the manually added devices (remove list from persistent storage).

Firmware Versions:

Model E

Microcontroller : V2.2
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.12 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.6

DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.12 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.12
FPGA : V3.1
CP-J : V2.12 Standalone and browser application

Model E-3

Microcontroller : V1.01
FPGA : V0.3
CP-J : V2.12 Standalone and browser application

Dugan-MY16

Microcontroller : V1.07
FPGA : V1.2
CP-J : V2.12 Standalone and browser application

CP-i : V1.0

Date: July 9th, 2012

Bug Fixes:

Model E-1

1.Normal/slave mode switch affected the Analog/Digital mode switch when viewed from Dugan-CPx when the unit is already in Digital mode. Firmware version bumped to V1.6.

DuganCP-J

1.Changed Model E-3 signal and music reference bargraph range.
2.Model E-3 Auxiliary output mixer output labels changed.

Firmware Versions:

Model E

Microcontroller : V2.2
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.11 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.6
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.11 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.11
FPGA : V3.0
CP-J : V2.11 Standalone and browser application

Model E-3

Microcontroller : V1.00
FPGA : V0.2
CP-J : V2.11 Standalone and browser application

Dugan-MY16

Microcontroller : V1.07
FPGA : V1.1
CP-J : V2.11 Standalone and browser application

CP-i : V1.0

Date: May 7th, 2012

Known Issues:

1.Apple iPad App Dugan CP-i displays wrong NOM gain limits for Model E-3 when CP-2 is connected to the unit.

Bug Fixes:

DuganCP

- 1.Fixed the bug in Version 2.08 and 2.09 that caused a crash when running as a Java Applet inside a browser.
- 2.Model E-3 NOM gain limit displayed wrong value when CP-2 is connected.

Model E-3

- 1.Passing wrong NOM gain limit values to CP-J or CP-i when CP-2 is connected.
- 2.Version bumped to 1.0 for official release of product.

Firmware Versions:

Model E

Microcontroller : V2.2
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.10 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.5
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.10 Standalone application

Model E-1A (Serial Number >= 417)

Microcontroller : V3.11
FPGA : V3.0
CP-J : V2.10 Standalone and browser application

Model E-3

Microcontroller : V1.00
FPGA : V0.2
CP-J : V2.10 Standalone and browser application

Dugan-MY16

Microcontroller : V1.07
FPGA : V1.1
CP-J : V2.10 Standalone and browser application

CP-i : V1.0

Date: April 18th, 2012

Utility

1. Added support for Model E-3.

DuganCP

1. Added full support for Model E-3.
2. Changed Link ID to System ID. System ID is now from 1 to 16.
3. Fixed legacy/native reversal problem.

Model E-1A

1. Link ID is now System ID. Default is set to 1.
2. Front panel channel Mute not reset when System Preset is triggered. This is now fixed.

Dugan-MY16

1. Link ID is now System ID. Default is set to 1.

Model E-3

1. Added to the distribution.

Firmware Versions:

Model E

Microcontroller : V2.2
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.09 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.5
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.09 Standalone application

Model E-1A (Serial Number \geq 417)

Microcontroller : V3.11
FPGA : V3.0
CP-J : V2.09 Standalone and browser application

Model E-3

Microcontroller : V0.33
FPGA : V0.2
CP-J : V2.09 Standalone and browser application

Dugan-MY16

Microcontroller : V1.07
FPGA : V1.1
CP-J : V2.09 Standalone and browser application

Date: March 21st, 2012

Utility

1.Prevent firmware update when a device is not reachable.

DuganCP

- 1.Updated to version 2.08 with a complete face lift.
- 2.Window is now resizable to allow display of more than 8 channels.
- 3.Switching to a difference device by clicking the device in the detected list no longer move the window back to the top left corner of the screen.
- 4.A bunch of small bug fixes.
- 5.Support link group for new Model E-1A and Dugan-MY16 firmwares. group b and c master mute/override/preset cannot be passed across units over the optical light pipe when multiple units are linked together. They need to pass over the network with multicast communication. Units linked together with ADAT light pipes need to belong to the same link group.
- 6.Remembers manually added devices across sessions.
- 7.Model E-1A with serial number \geq 417 now displays as Model E-1A.

Dugan-MY16

- 1.FPGA updated to V1.1 to improve robustness of the audio transport channels.
- 2.Fixed consecutive ftp file transfer bug.
- 3.Minor change in the DSP engine regarding safety threshold.
- 4.Fixed "doesn't remember new unit name" bug.
- 5.Turned on multicast support.
- 6.Master mute, msater preset and master override are now group specific. However, only group 1 can be passed across the ADAT link to slaves. Use the new Link Group feature in the CP-J to group units together.

7. When master mute/override is active, channel group assignment change works correctly now.

Model E-1A with Serial Number ≥ 417 , now shown in Dugan Control Panel as Model E-1A

1. Fixed the front panel BYPASS bug that caused bypass failure if more than one channel is in bypass mode.

2. Fixed bug that caused bypassed channel to affect the automix of the un-bypassed channels.

3. Added multi-group supports. Channels now can be partitioned into groups, a, b and c as in the D-2, D-3 and Dugan-MY16.

4. Master mute, master preset and master override are group specific. However, only group 1 can be passed across the ADAT link to slaves. Use the new Link Group feature in the CP-J to group units together.

5. If linking is not required, both ADAT interfaces can be used for Dugan Auto Mixing, i.e., a 16-channel Dugan Auto mixing system. From the front panel, hold down channel 4 Bypass button while switching on the unit to turn on this feature, or hold down channel 4 Mute button while switching on the unit to turn off this feature. It can also be turned on/off from the latest Dugan CP-J.

6. When master mute/override is active, channel group assignment change works correctly now.

Firmware Versions:

Model E

Microcontroller	: V2.2
DSP	: V1.7
WebUI	: V1.5 Browser application
CP-J	: V2.08 Standalone application

Model E-1A Serial Number < 417

Microcontroller	: V1.5
DSP	: V1.7
WebUI	: V1.5 Browser application
CP-J	: V2.08 Standalone application

Model E-1A (Serial Number ≥ 417)

Microcontroller	: V3.10
FPGA	: V3.0
CP-J	: V2.08 Standalone and browser application

Dugan-MY16

Microcontroller	: V1.06
FPGA	: V1.1
CP-J	: V2.08 Standalone and browser application

Date: October 24th, 2011

Model E-1A with serial number ≥ 417

1. version updated to 3.05. Fixed DSP bug that caused the channel gain display to be stuck high when a channel is switched to MUTE mode. Bug was introduced in version 3.01.

Firmware Versions:

Model E

Microcontroller : V2.2
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.07 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.5
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.07 Standalone application

Model E-1A Serial Number >= 417

Microcontroller : V3.05
FPGA : V3.0
CP-J : V2.07 Standalone and browser application

Dugan-MY16

Microcontroller : V1.05
FPGA : V1.0
CP-J : V2.07 Standalone and browser application

Date: October 20th, 2011

Firmware Updater

1.Added support for the newly revised Model E-1A. This will only affect Model E-1A with serial number 417 or higher.

Firmware Versions:

Model E

Microcontroller : V2.2
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.07 Standalone application

Model E-1A Serial Number < 417

Microcontroller : V1.5
DSP : V1.7
WebUI : V1.5 Browser application
CP-J : V2.07 Standalone application

Model E-1A Serial Number >= 417

Microcontroller : V3.04
FPGA : V3.0

CP-J : V2.07 Standalone and browser application

Dugan-MY16

Microcontroller : V1.05

FPGA : V1.0

CP-J : V2.07 Standalone and browser application

Date: September 6th, 2011

Firmware Updater

1. Occasionally the FPGA file would be downloaded but the firmware download would fail.

DuganCP-J - updated to Version 2.07 with couple of bug fixes:

1. Inconsistent display of the list of detected devices. Sometimes multiple detected devices might be displayed on top of each other, and some other times some devices might be displayed as a blank slot.
2. Inconsistent sorting of the detected device list. Detected devices are supposed to be displayed from left to right in alphabetical order based on the unit name. Sometimes the sorting was incorrect. Also changing the unit name did not trigger a re-sorting of the list.

Firmware Versions:

Model E

Microcontroller : V2.2

DSP : V1.7

WebUI : V1.5

Model E-1A

Microcontroller : V1.5

DSP : V1.7

WebUI : V1.5

Dugan-MY16

Microcontroller : V1.05

FPGA : V1.0

CP-J : V2.07

Date: August 24th, 2011

There is no change in firmware for any of the devices. The updater itself is updated with couple of bug fixes:

1. FPGA code did not get updated for Dugan-MY16 cards.
2. When only the FPGA is updated for Dugan-MY16 cards, the updater failed to reboot the card. So the updater would display the card with the old FPGA version.

Firmware Versions:

Model E

Microcontroller	: V2.2
DSP	: V1.7
WebUI	: V1.5

Model E-1A

Microcontroller	: V1.5
DSP	: V1.7
WebUI	: V1.5

Dugan-MY16

Microcontroller	: V1.05
FPGA	: V1.0
CP-J	: V2.06